



SSCE4V542N1

1-line Bidirectional Micro Packaged TVS Diodes for ESD Protection

● Description

The SSCE4V542N1 is a bi-directional TVS diode. It is designed with AF process TVS technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium. It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD (electrostatic discharge), and EFT (electrical fast transients).

● Feature

- ✧ 96W peak pulse power ($t_P = 8/20\mu s$)
- ✧ DFN1006-2L Package
- ✧ Working voltage: 4.5V
- ✧ Low clamping voltage
- ✧ Low capacitance
- ✧ Low leakage current
- ✧ Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30kV$
 - Contact discharge: $\pm 30kV$
 - IEC61000-4-5 (Lightning) 8A (8/20 μs)

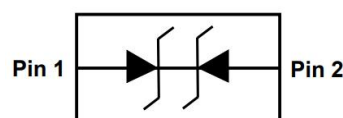
● Applications

- ✧ Cellular Handsets and Accessories
- ✧ Personal Digital Assistants
- ✧ Notebooks and Handhelds
- ✧ Portable Instrumentation
- ✧ Digital Cameras
- ✧ Peripherals
- ✧ Audio Players
- ✧ Keypads, Side Keys, LCD Displays

● PIN configuration



DFN1006-2L (Bottom View)



Circuit Diagram



Marking

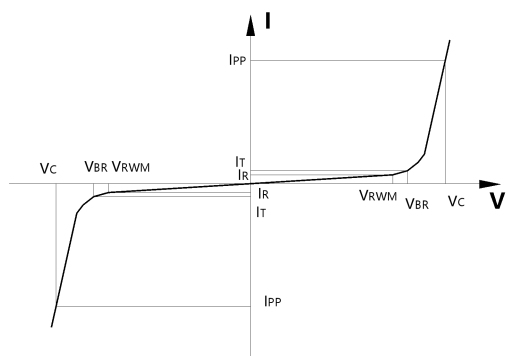
● Mechanical data

- ✧ Package: DFN1006-2L(1.0×0.6×0.5mm)
- ✧ Lead finish: 100% matte Sn (Tin)
- ✧ Device meets MSL 3 requirements
- ✧ Case Material: "Green" Molding Compound
- ✧ RoHS Compliant
- ✧ Pure tin plating: 7~17 μm
- ✧ Pin flatness: $\leq 3mil$



● Electronic Parameter

Symbol	Parameter
V_{RWM}	Peak Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
P_{PP}	Peak Pulse Power
C_J	Junction Capacitance



● Absolute maximum rating ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	P_{PP}	96	W
Peak Pulse Current (8/20 μs)	I_{PP}	8	A
ESD Rating per IEC61000-4-2: Contact Air	V_{ESD}	± 30 ± 30	kV
Storage Temperature	T_{STG}	-55/+150	$^{\circ}\text{C}$
Operating Temperature	T_J	-55/+125	$^{\circ}\text{C}$

● Electrical Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

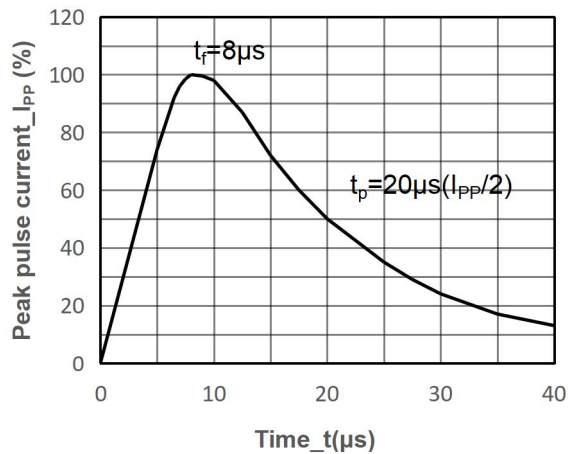
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Peak Reverse Working Voltage	V_{RWM}				4.5	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	4.7		6.8	V
Reverse Leakage Current	I_R	$V_{RWM} = 4.5\text{V}$			1	μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$, $t_P = 8/20\mu\text{s}$		6		V
Clamping Voltage	V_C	$I_{PP} = 8\text{A}$, $t_P = 8/20\mu\text{s}$		8	12	V
ESD Clamping Voltage(Note1)	V_{CL-ESD}	IEC 61000-4-2+ 8kV($I_{TLP}=16\text{A}$), contact mode, $T=25^{\circ}\text{C}$, pin1 to pin2, pin2 to pin1		8		V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		13	20	pF

Note 1: ESD Clamping Voltage was measured by Transmission Line Pulsing (TLP) System.

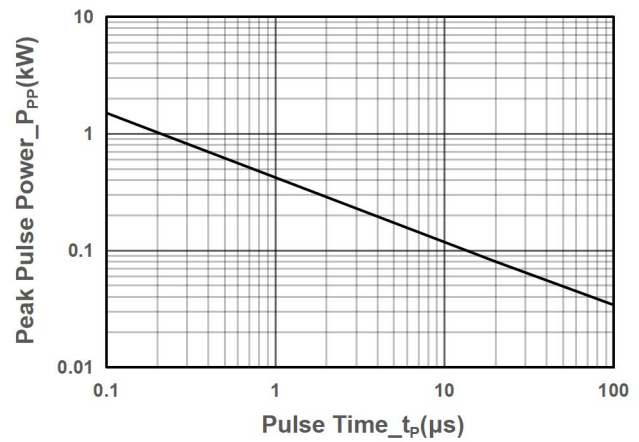
TLP conditions: $Z_0=50\Omega$, $t_p=100\text{ns}$, $t_r=1\text{ns}$.



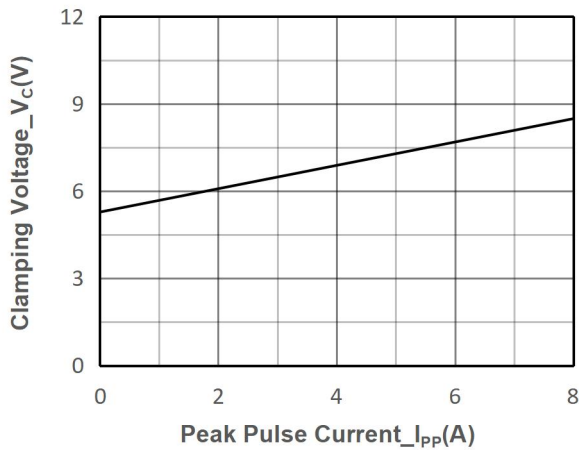
- Typical Performance Characteristics ($T_A=25^{\circ}\text{C}$ unless otherwise noted)



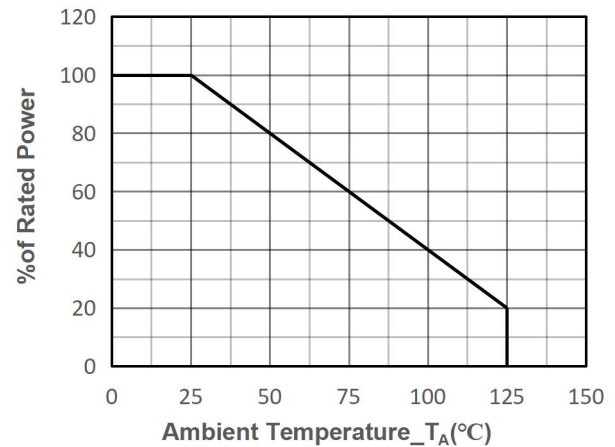
8/20 μs Pulse Waveform



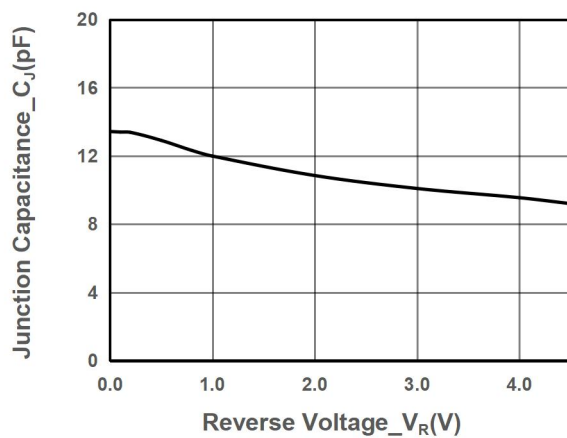
Peak Pulse Power vs. Pulse Time



Clamping Voltage vs. Peak Pulse Current



Power derating vs. Ambient temperature



Junction Capacitance vs. Reverse Voltage



● Package Information

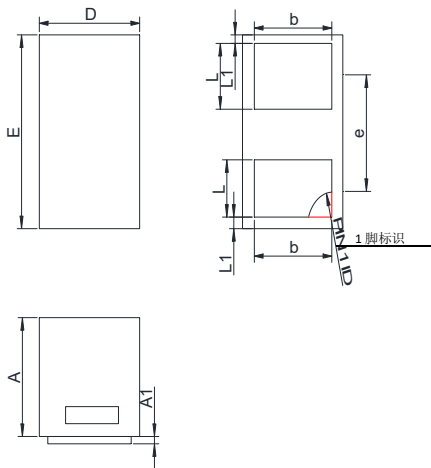
Ordering Information

Device	Package	Qty per Reel	Reel Size
SSCE4V542N1	DFN1006-2L	10000	7 Inch

Mechanical Data

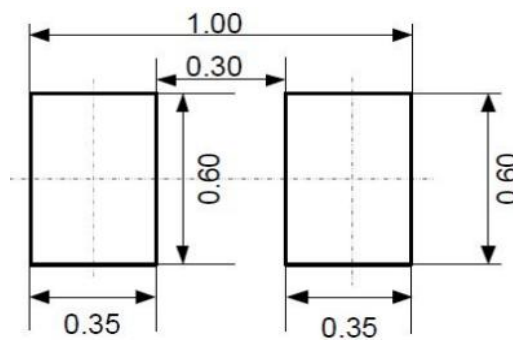
Case: DFN1006-2L

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters	
	Min	Max
A	0.43	0.55
A1	0.00	0.05
D	0.55	0.65
E	0.95	1.05
b	0.45	0.60
e	0.65TYP	
L	0.2	0.3
L1	0.05REF	

Recommended Pad outline (Unit: mm)





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